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•Comments:

Please find attached the following documents:

- 1. Request for Corrected Filing Receipt, and
- 2, Copy of the Filing Receipt dated April 17, 2007.

Respectfully submitted,

/Joseph R. Keating #37,368/

Joseph R. Keating (Registration Number 37,368)

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I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)]

Date: May 23, 2007

/Rebecca L. Cordeau/ Rebecca L. Cordeau DOCKET NUMBER 70404.107/sa

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Yasunobu TAGUSA

Application No.: 10/598,488

Confirmation No.: 6247

Filing or 371(c) Date: August 31, 2006

Title: METHOD FOR FABRICATING A SEMICONDUCTOR DEVICE AND

APPARATUS FOR INSPECTING A

SEMICONDUCTOR

Art Unit: 2812

Examiner:

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants respectfully request that a corrected Filing Receipt be issued.

Applicants have provided a copy of the Filing Receipt with the corrections to be made noted thereon.

Respectfully submitted,

Dated: May 23, 2007

KEATING & BENNETT, LLP 8180 Greensboro Drive, Suite 850 Tyson's Corner, VA 22102 Telephone: (703) 637-1480 Facsimile: (703) 637-1499 /Stephen R. Funk #57,751/
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APPL NO.	FILING OR 371(c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	TOT CLMS	IND CLMS	
			<u></u>	70404 407/00	22	2	
10/598 488	08/31/2006	2812	1000	70404.107/sa		-	

CONFIRMATION NO. 6247

54072
SHARP KABUSHIKI KAISHA
C/O KEATING & BENNETT, LLP
8180 GREENSBORO DRIVE
SUITE 850
MCLEAN, VA 22102

FILING RECEIPT
OC000000023386786

Date Mailed: 04/17/2007

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Yasunobu Tagusa, Nara, JAPAN; IKoma-Shi

Assignment For Published Patent Application

SHARP KABUSHIKI KAISHA, Osaka-shi, Osaka, JP

Power of Attorney: The patent practitioners associated with Customer Number 54072.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP05/04036 03/02/2005

Foreign Applications

JAPAN 2004-060511 03/04/2004

If Required, Foreign Filing License Granted: 04/13/2007

The country code and number of your priority application, to be used for filing abroad under the Parls Convention, is US10/598,488

Projected Publication Date: 07/26/2007

Non-Publication Request: No

Early Publication Request: No

Title

Method for fabricating a semiconductor device and apparatus for inspecting a semiconductor

Preliminary Class

438

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